

FORM PTO-892 (REV. 2-92)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	SERIAL NO. 08/187675	GROUP/PART UNIT 2316	ATTACHMENT TO PAPER NUMBER 3
NOTICE OF REFERENCES CITED		APPLICANT(S) Donald F. Ault		

U.S. PATENT DOCUMENTS

*	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
A	5 2 9 7 2 7 4	Mar. 22, 94	Jackson	395	500	Apr. 15, 91
B	5 3 0 5 1 5 5	Apr. 19, 94	Anschuetz et al.	395	700	Dec. 21, 90
C	5 2 4 7 6 7 5	Sp. 21, 93	Fairwell et al.	395	650	Aug. 9, 91
D	5 0 1 2 4 0 9	Apr. 30, 91	Fletcher et al.	364	200	Mar. 10, 88
E	5 4 0 4 5 2 1	Apr. 4, 95	Muiray	395	650	Jul. 31, 90
F	5 3 1 9 7 8 2	Jun. 7, 94	Goldberg et al.	395	650	Jan. 31, 89
G						
H						
I						
J						
K						

FOREIGN PATENT DOCUMENTS

*	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHOTS PP. DWG. SPEC.	
L								
M								
N								
O								
P								
Q								

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

R	Performance of Multi-Thread execution in a shared-memory
S	Multi-processor, preceding of The Third IEEE Symposium
	on parallel and distributed processing; IEEE Comp. Soc.
	press, Los Alamitos, CA, USA.
T	
U	

EXAMINER

DATE

Majid A. Bananmehal Apr. 14, 1995

* A copy of this reference is not being furnished with this office action.
(See Manual of Patent Examining Procedure, section 707.05 (a).)